

What is claimed is:

1. A panel driver for driving a liquid crystal display device using a super twisted nematic STN mode, comprising:

5 at least one first means for receiving a plurality of source voltages and supplying a selected source voltage among a plurality of driving power lines; and

10 at least one second means connected to the plurality of driving power lines for delivering the selected source voltage to a line.

2. The panel driver as recited in claim 1, further comprising:

 a first driving power line; and

15 a second driving power line,

 wherein said first means outputs the selected source voltage to one of a first and a second driving power lines.

3. The panel driver as recited in claim 2, wherein the
20 second means includes

 a first transistor controlled by the first driving voltage control signal for transmitting the selected source voltage supplied at the first driving power line to the line; and

25 a second transistor controlled by the second driving voltage control signal for transmitting the selected source voltage supplied at the second driving power line to the line.

4. The panel driver as recited in claim 3, wherein the
30 first means includes

 a plurality of voltage following means for delivering the plurality of source voltages; and

a multiplexing means for receiving the plurality of source voltages from the plurality of voltage following means and outputting the selected source voltage to one of the first and the second driving power lines in response to a power selecting signal.

5. The panel driver as recited in claim 3, wherein the first means includes

a multiplexing means for receiving the plurality of source voltages and outputting the selected source voltage in response to a power selecting signal; and

a plurality of voltage following means for delivering the selected source voltage to one of the first and the second driving power lines.

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6. A panel driver for driving a liquid crystal display device using a super twisted nematic STN mode, comprising:

at least one first supplying means for receiving a plurality of first source voltages and respectively supplying a first selected source voltage to one of a first and a second driving power lines;

at least one second supplying means for receiving a plurality of second source voltages and respectively supplying a second selected source voltage to one of a third and a fourth driving power lines;

at least one first driving means for receiving the first selected source voltage and driving a segment line in response to first and second driving voltage control signals; and

at least one second driving means for receiving the second selected source voltage and driving a common line in response to third and fourth driving voltage control signals.

7. The panel driver as recited in claim 6, wherein the first supplying means includes

a plurality of voltage following means for delivering the plurality of first source voltages; and

5 a multiplexing means for receiving the plurality of first source voltages from the plurality of voltage following means and outputting the first selected source voltage to one of the first and the second driving power lines in response to a first power selecting signal.

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8. The panel driver as recited in claim 7, wherein the second supplying includes

a plurality of voltage following means for delivering the plurality of second source voltages; and

15 a multiplexing means for receiving the plurality of second source voltages from the plurality of voltage following means and outputting the second selected source voltage to one of the third and the fourth driving power lines in response to a second power selecting signal.

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9. The panel driver as recited in claim 6, wherein the first supplying includes

a multiplexing means for receiving the plurality of first source voltages and outputting the first selected source voltage in response to a first power selecting signal; and

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a plurality of voltage following means for delivering the first selected source voltage to one of the first and the second driving power lines.

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10. The panel driver as recited in claim 9, wherein the second supplying means includes

a multiplexing means for receiving the plurality of

second source voltages and outputting the second selected source voltage in response to a second power selecting signal; and

5 a plurality of voltage following means for delivering the second selected source voltage to one of the third and the fourth driving power lines.

11. The panel driver as recited in claim 6, wherein the first driving means includes

10 a first line driver controlled by the first driving voltage control signal for transmitting the first selected source voltage supplied at the first driving power line to the segment line; and

15 a second line driver controlled by the second driving voltage control signal for transmitting the first selected source voltage supplied at the second driving power line to the segment line.

12. The panel driver as recited in claim 11, wherein the first line driver includes a first MOS transistor for delivering the first selected source voltage supplied at the first driving power line to the segment line, a gate of the first MOS transistor being connected to the first driving voltage control signal.

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13. The panel driver as recited in claim 12, wherein the second line driver includes a second MOS transistor for delivering the first selected source voltage supplied at the second driving power line to the segment line, a gate of the second MOS transistor being connected to the second driving voltage control signal.

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14. The panel driver as recited in claim 11, wherein the second driving means includes

a third line driver controlled by the third driving voltage control signal for receiving the second selected source voltage supplied to the third driving power line; and

a fourth line driver controlled by the fourth driving voltage control signal for receiving the second selected source voltage supplied to the fourth driving power line.

10 15. The panel driver as recited in claim 14, wherein the third line driver includes a third MOS transistor for delivering the second selected source voltage supplied at the third driving power line to the common line, a gate of the third MOS transistor being connected to the third driving voltage control signal.

16. The panel driver as recited in claim 15, wherein the fourth line driver includes a fourth MOS transistor for delivering the second selected source voltage supplied at the fourth driving power line to the common line, a gate of the fourth MOS transistor being connected to the fourth driving voltage control signal.